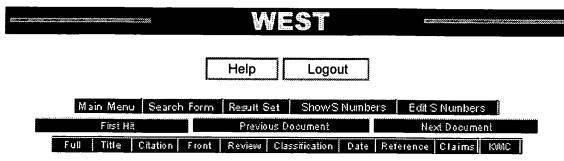


DB Name	Query	Hit Count	Set Name
JPAB,EPAB,DWPI	19 and (bottom-up)	0	<u>L10</u>
JPAB,EPAB,DWPI	17 and script	191	<u>L9</u>
JPAB,EPAB,DWPI	17 and netlist and script	0	<u>L8</u>
JPAB,EPAB,DWPI	circuit synthesis	2051299	<u>L7</u>
JPAB,EPAB,DWPI	11	0	<u>L6</u>
JPAB,EPAB,DWPI	12 and (bottom-up) and (top-down)	0	<u>L5</u>
USPT	12 and (bottom-up) and (top-down)	19	<u>L4</u>
USPT	l2 and (bottom-up)	19	<u>L3</u>
USPT	11 and netlist and script	30	<u>L2</u>
USPT	(circuit or logic) same synthesis	4147	<u>L1</u>



Document Number 72

Entry 72 of 191

File: DWPI

Sep 22, 1998

DERWENT-ACC-NO: 1998-531247

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TITLE: Computer based gate level specification generation method for design, verification and implementation of ASCI - involves generating synthesis script that specifies characterizing of synthesised lower level module according to user specified constraints

INVENTOR: ADKAR, S; GUPTE, V V

PATENT-ASSIGNEE: ; LSI LOGIC CORP[; LSILN]

PRIORITY-DATA:

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PATENT-FAMILY:

PUB-NO

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APPLICATION-DATA:

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APPL-NO

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INT-CL (IPC): G06F 17/50

ABSTRACTED-PUB-NO: US 5812416A

BASIC-ABSTRACT:

The method involves receiving the user-specified constraints and a programming language description of the IC. At least one synthesis script for the IC is generated.

The <u>synthesis script</u> specifies synthesizing of lower level modules and top level modules according to default constraints and user specified constraints, respectively. Further the <u>synthesis script</u> specifies characterizing of the synthesised lower level module according to user specified constraints.

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ADVANTAGE - Enables to store design information for utilization by design applications. Uses authorization file for restricting modification of design information without necessary security access.

CHOSEN-DRAWING: Dwg.14/19

TITL E-TERMS: COMPUTER BASED GATE LEVEL SPECIFICATION GENERATE METHOD DESIGN VERIFICATIO N IMPLEMENT GENERATE SYNTHESIS SCRIPT SPECIFIED SYNTHESIS LOWER LEVEL MODULE ACCORD USER SPECIFIED CONSTRAIN

DERWENT-CLASS: T01

EPI-CODES: T01-J15A1; T01-J15A2; T01-J15B;

SECONDARY-ACC-NO:

Non-CPI Secondary Accession Numbers: N1998-414526

